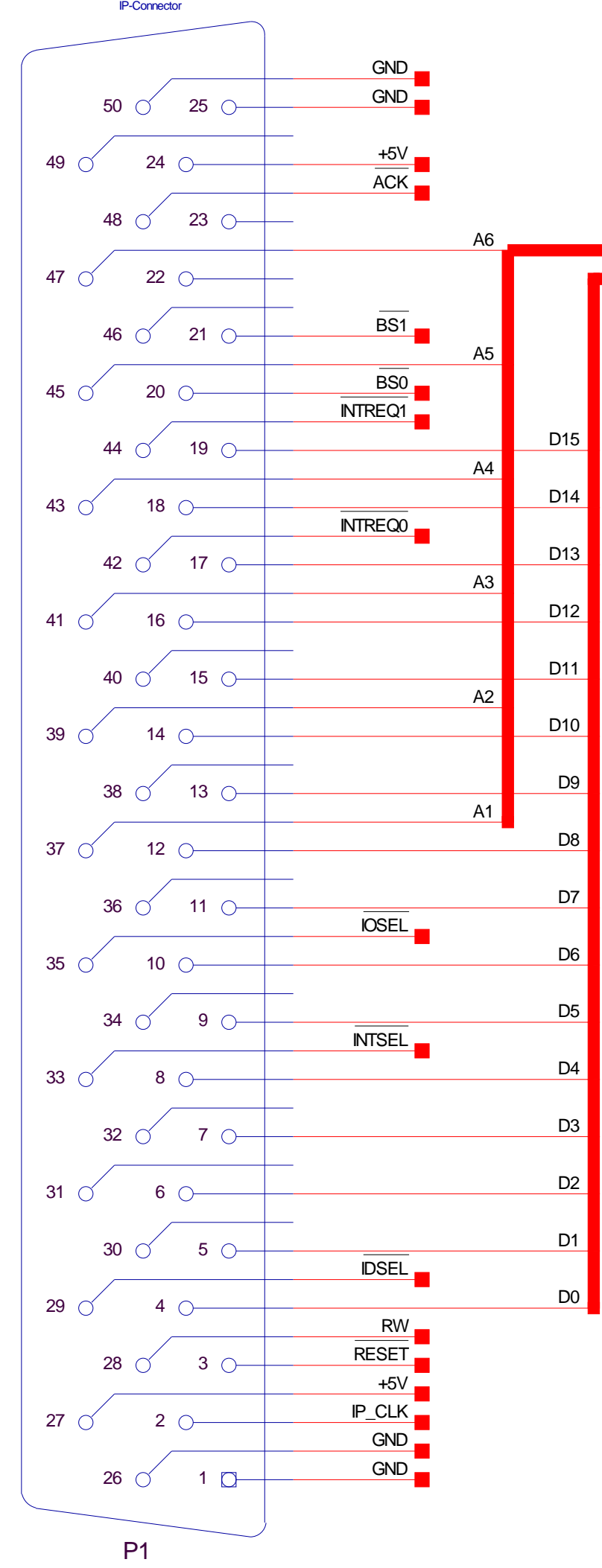
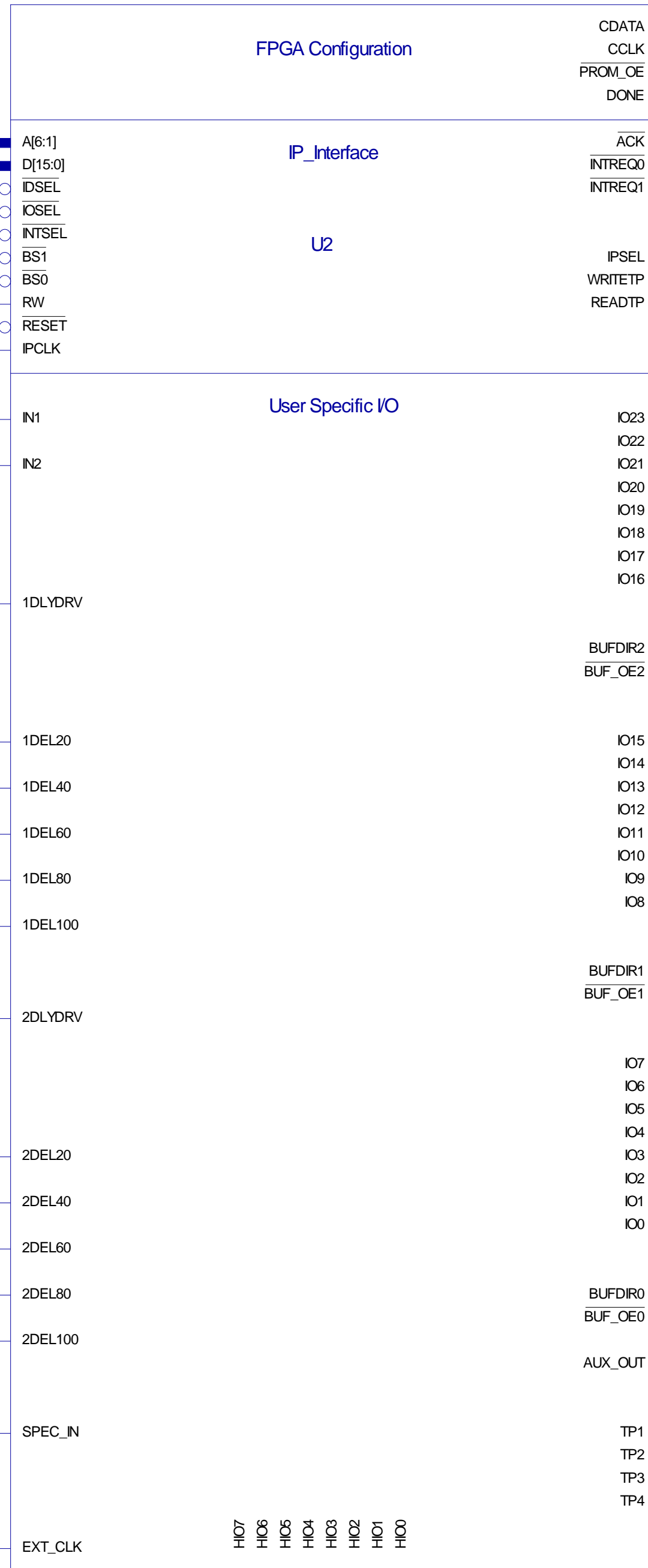


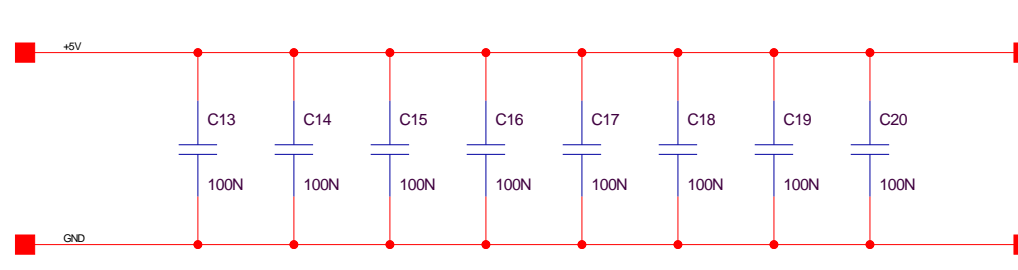
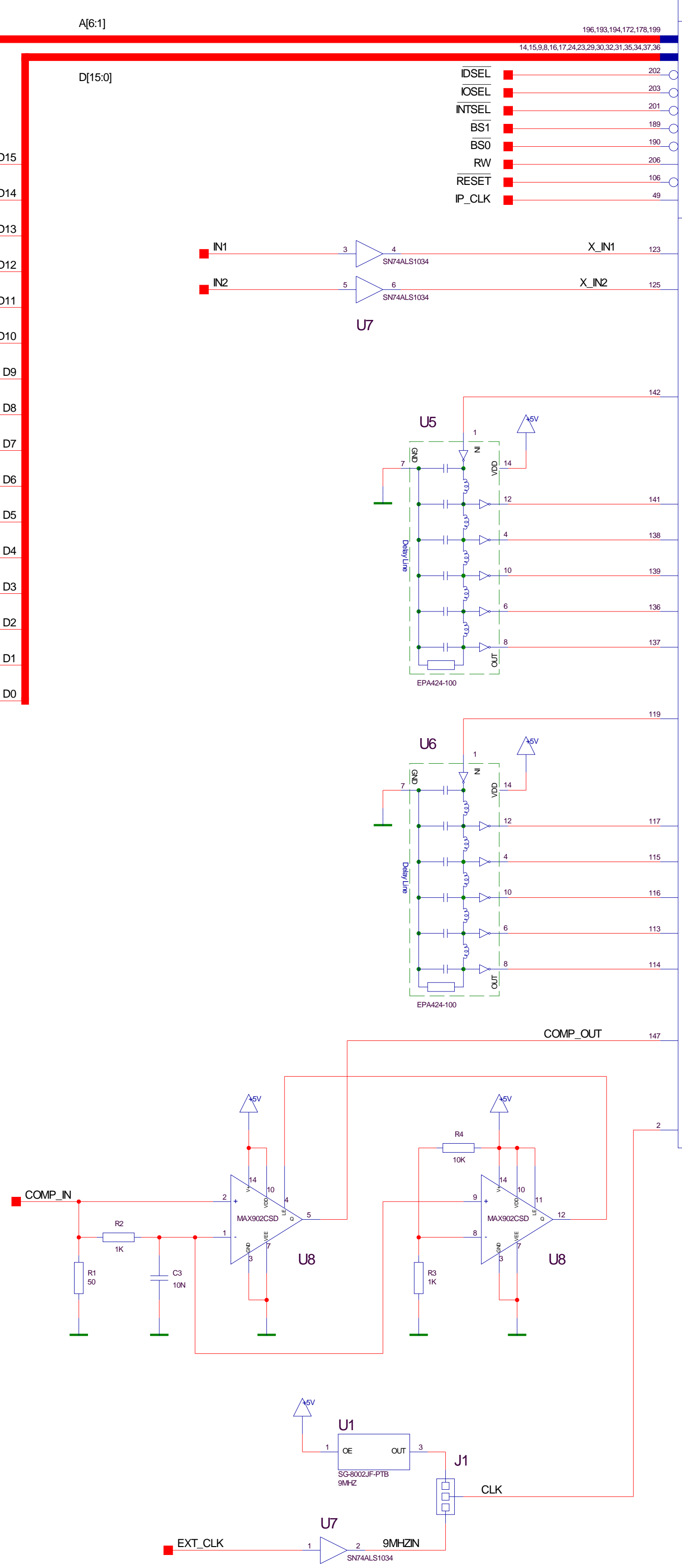
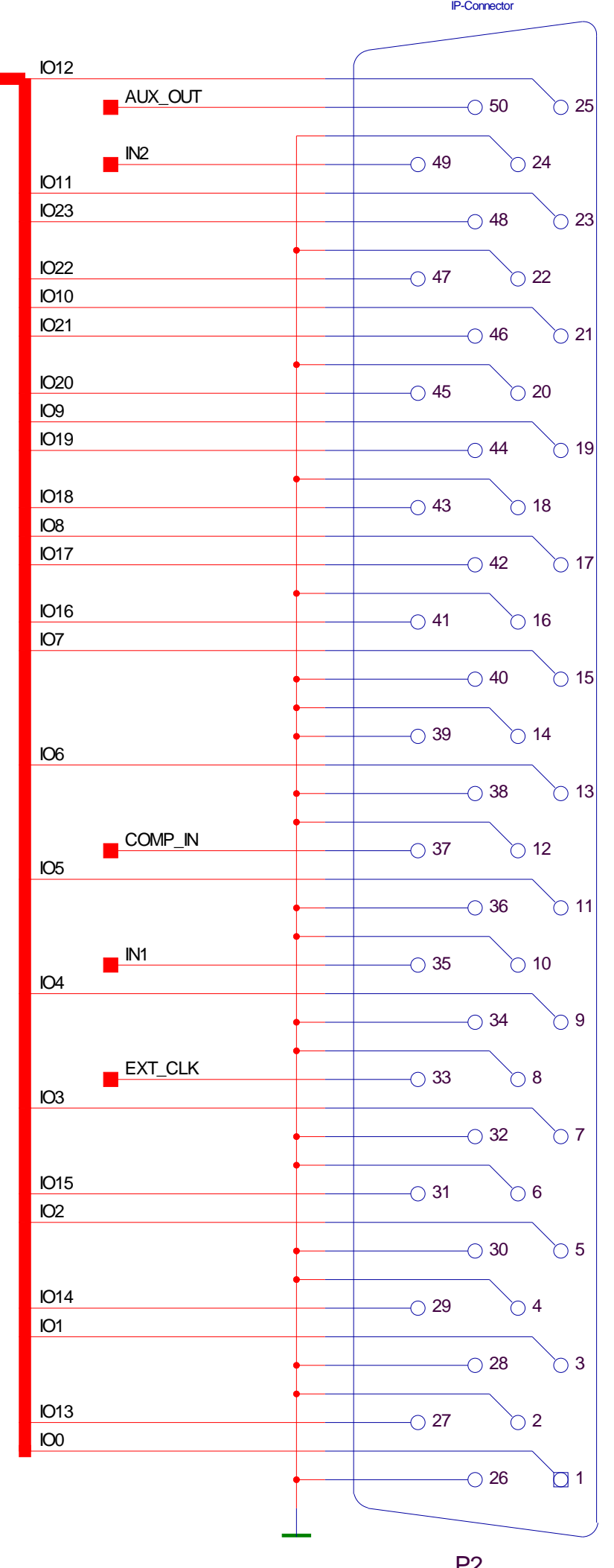
IP Interface Connector

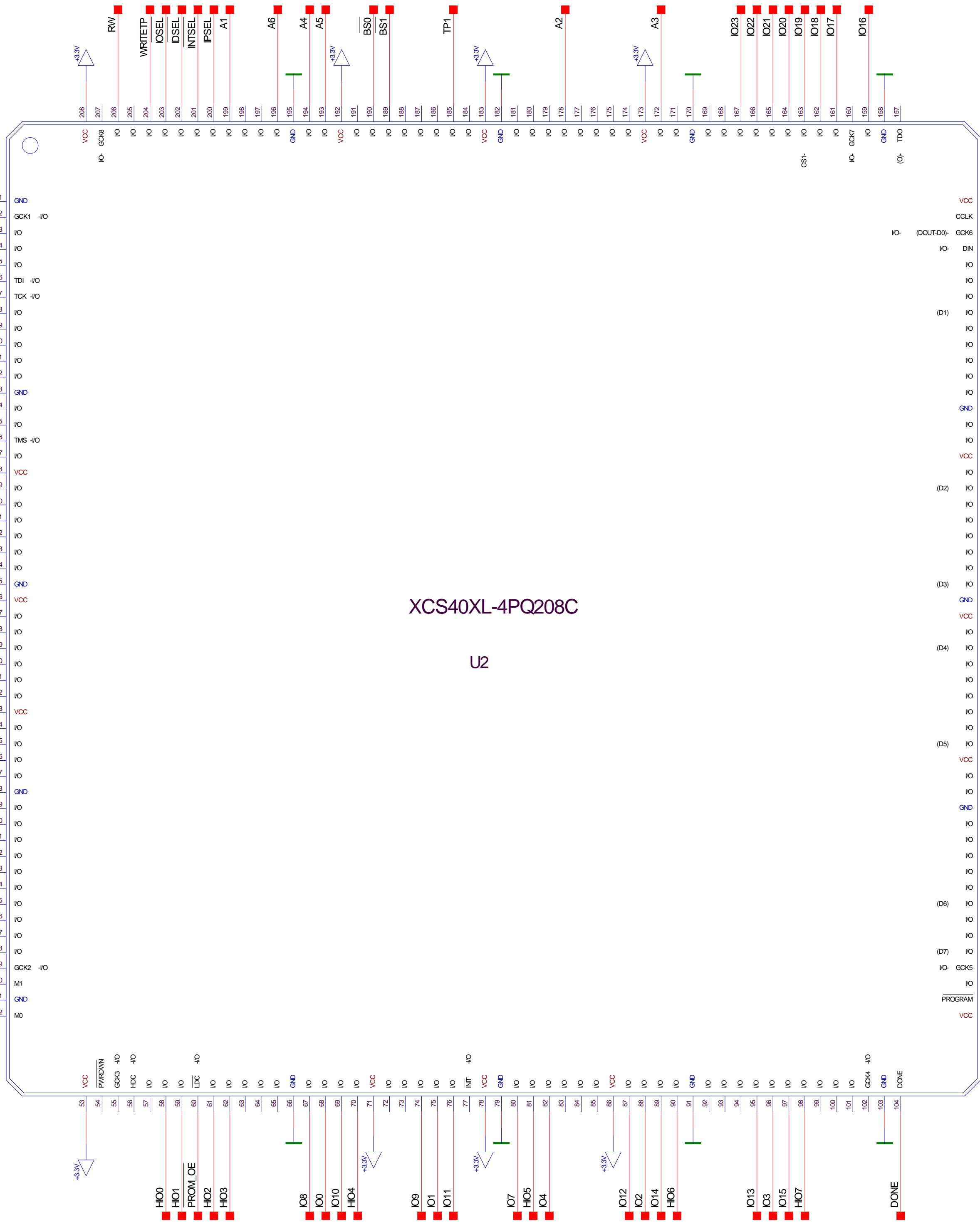


XILINX-SYM

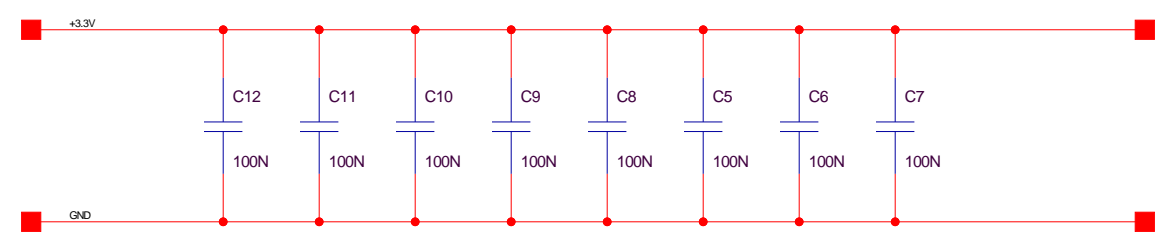


IP - IO - Connector





XCS40XL-4PQ208C  
U2



schematic: XILINX-SYM\_REV2



XILINX FPGA (Rev 2)

Drawing	F.TONSCH	Date:	7-19-2006_13:58	Sheet	1 of 1
Design	F.TONSCH				